

TITLE: NOVEL METHOD AND SYSTEM FOR INTERACTION BETWEEN A PROCESSOR AND A POWER ON  
RESET CIRCUIT TO DYNAMICALLY CONTROL POWER STATED IN MICROCONTROLLER

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USSN: 09/887,923

Attorney Docket #: CYPR-CD00229

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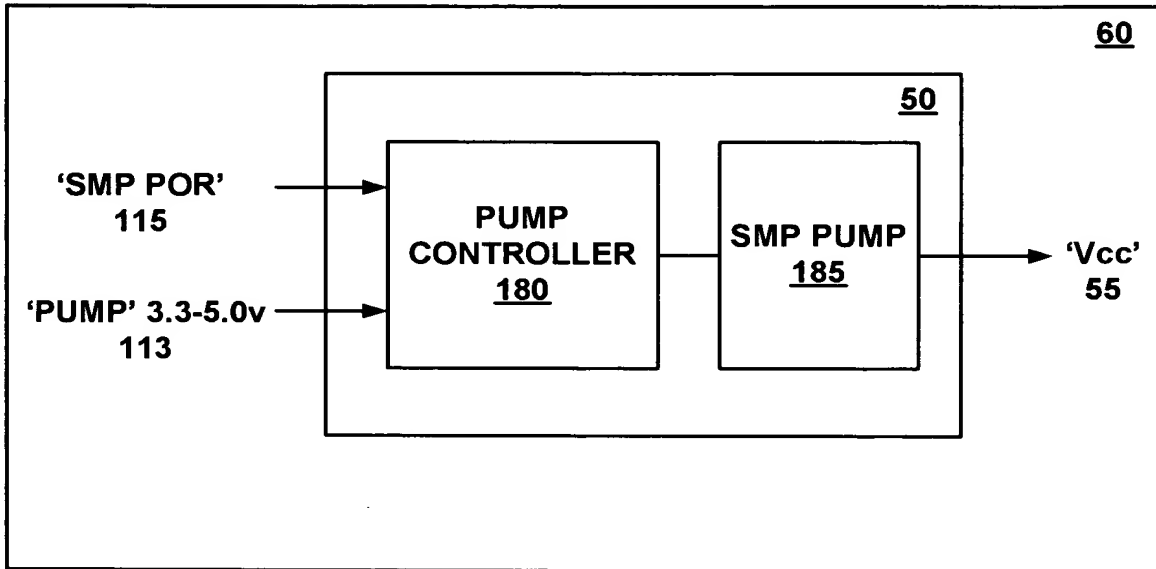


FIGURE 1

22

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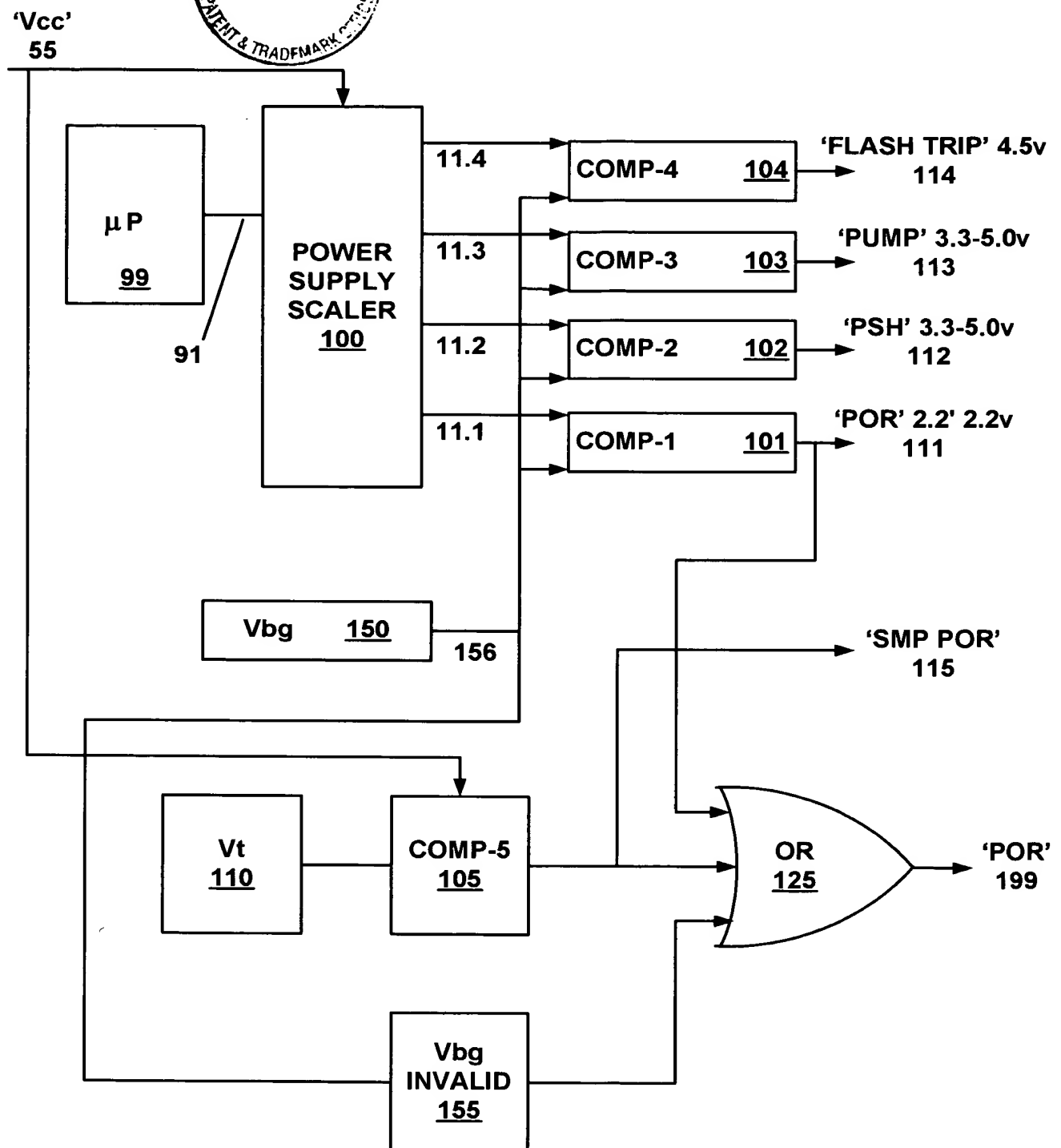
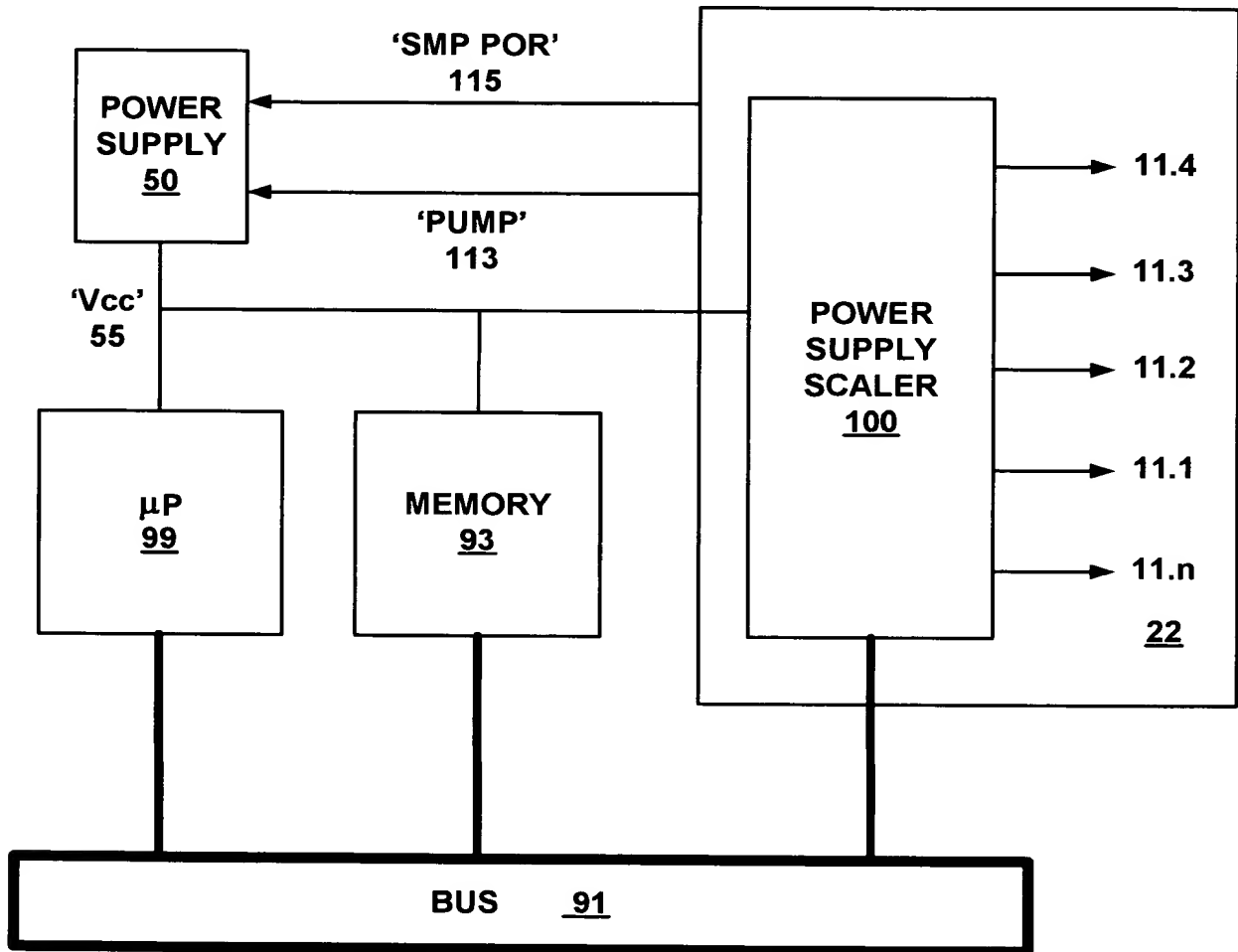


FIGURE 2

300



**FIGURE 3**

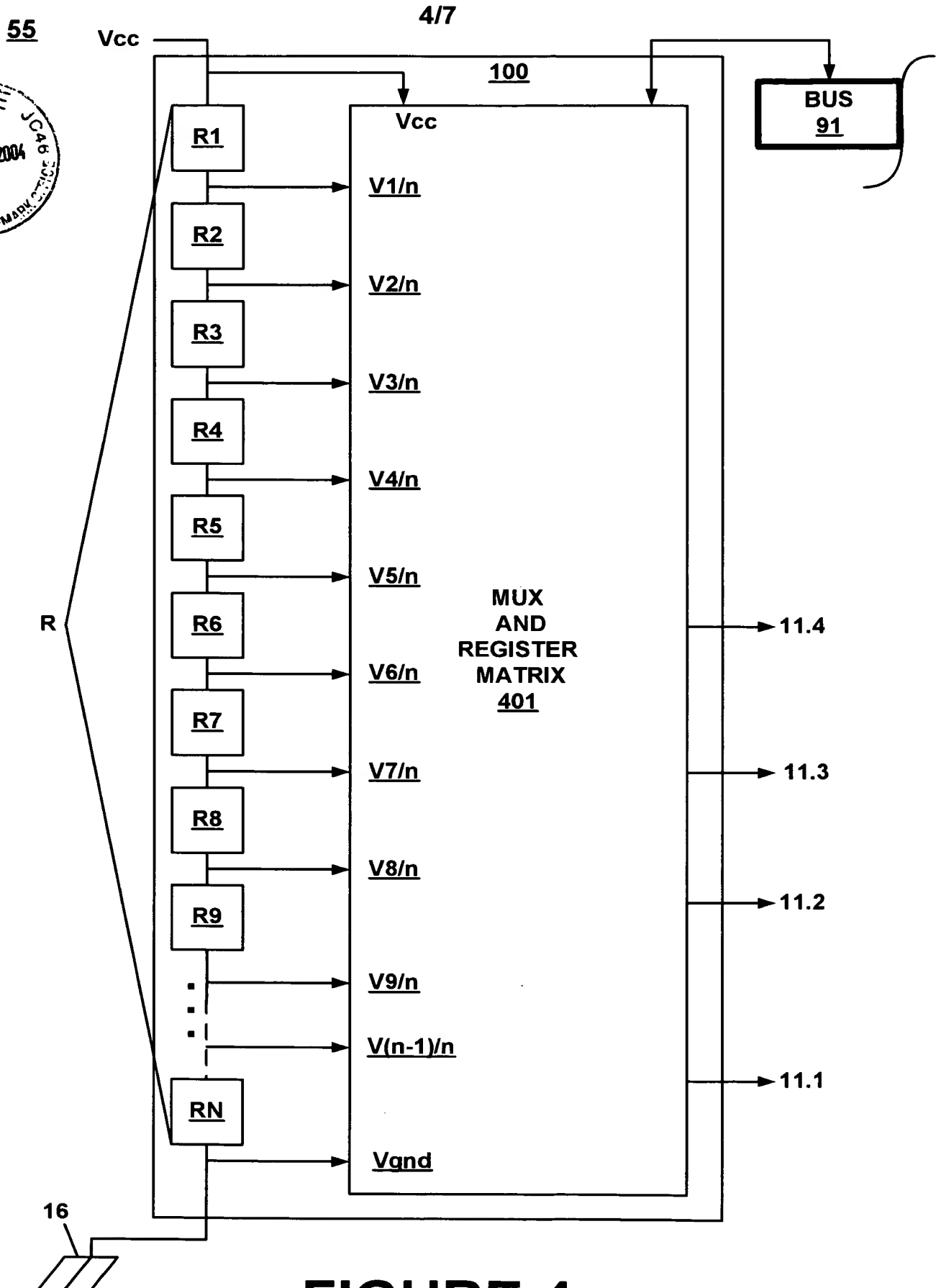


FIGURE 4

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500

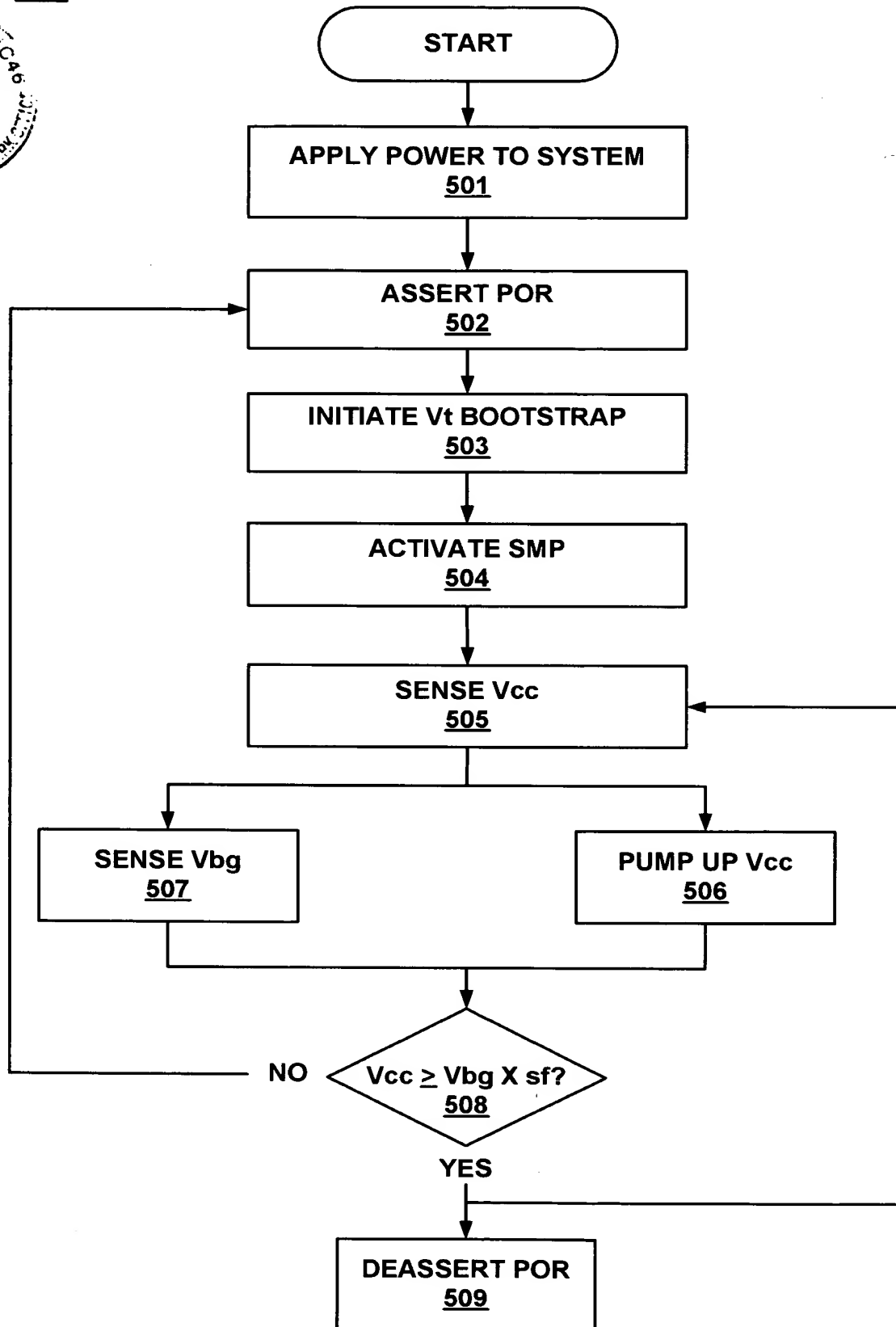


FIGURE 5

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600

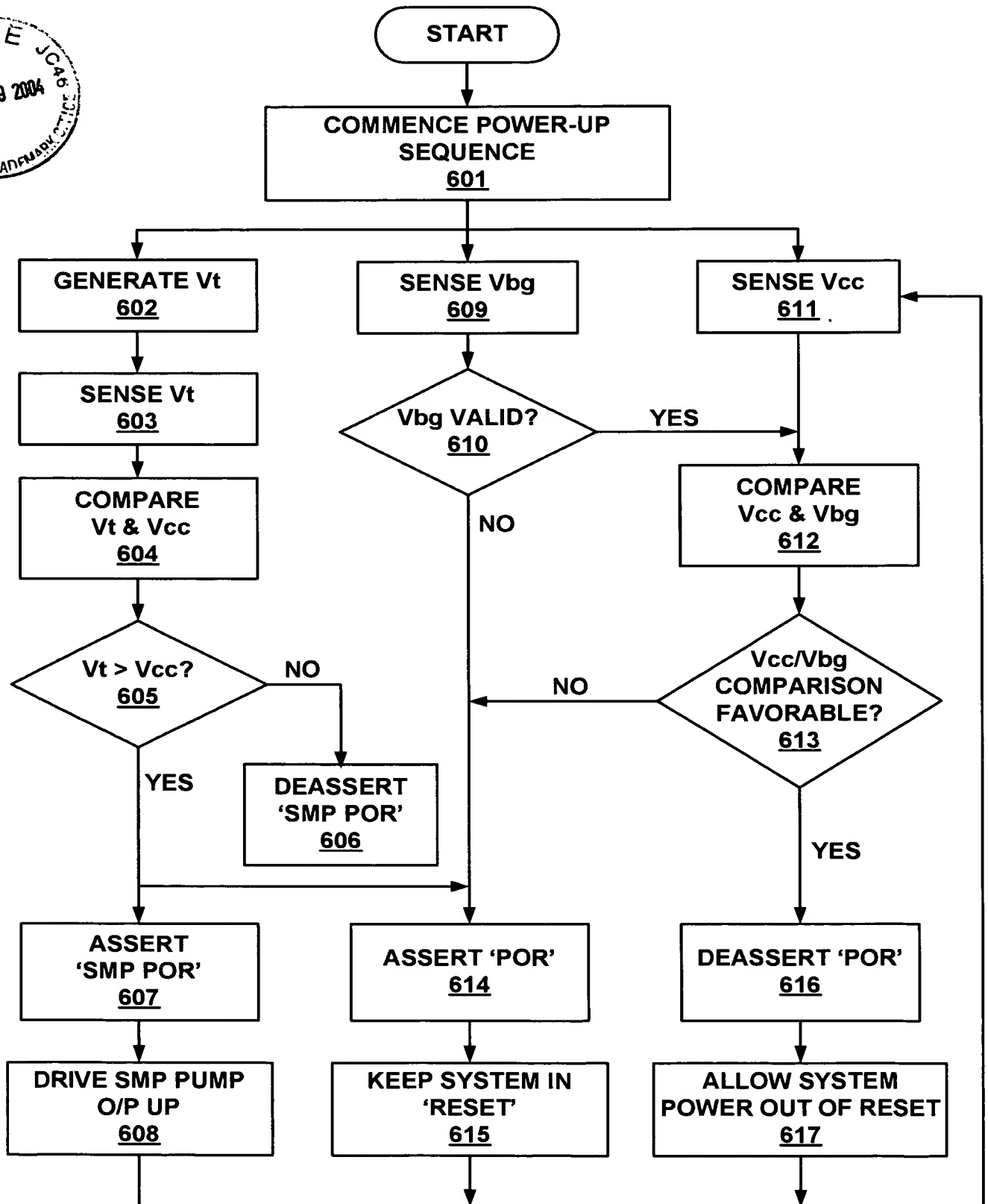
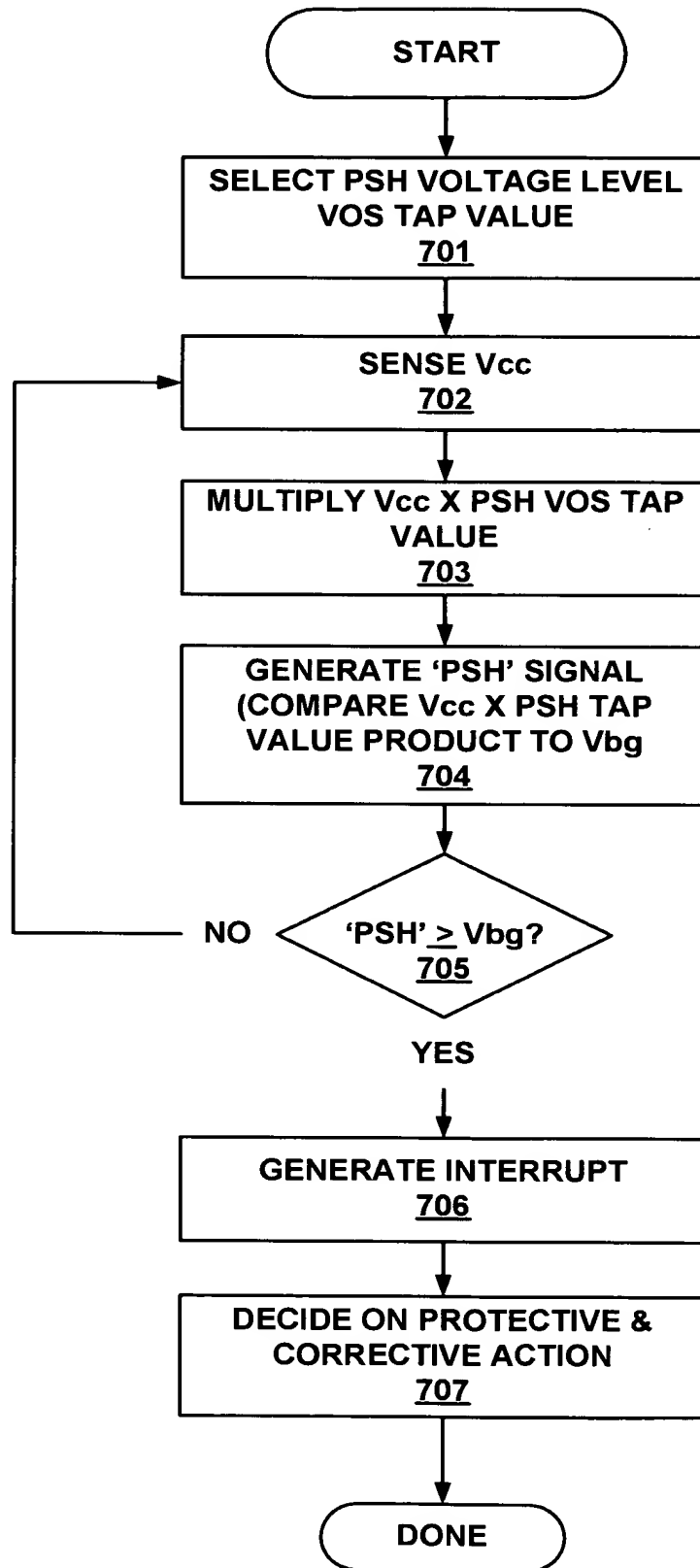


FIGURE 6

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700



**FIGURE 7**